

## Electronic Acknowledgement Receipt

<b>EFS ID:</b>	4827895
<b>Application Number:</b>	10083533
<b>International Application Number:</b>	
<b>Confirmation Number:</b>	6400
<b>Title of Invention:</b>	SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE INCLUDING A SEMICONDUCTOR DEVICE HAVING A STABLE THRESHOLD CHARACTERISTIC
<b>First Named Inventor/Applicant Name:</b>	Hiroshi Hashimoto
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<b>Filer Authorized By:</b>	Michael J. Caridi
<b>Attorney Docket Number:</b>	020244
<b>Receipt Date:</b>	20-FEB-2009
<b>Filing Date:</b>	27-FEB-2002
<b>Time Stamp:</b>	15:12:06
<b>Application Type:</b>	Utility under 35 USC 111(a)

### Payment information:

Submitted with Payment	no
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### File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Information Disclosure Statement (IDS) Filed (SB/08)	020244_IDS_Form_SB08_due_02_20_09.pdf	764430 f65d535235fd52501b224bc12dfe1d7fb03795f2	no	4

### Warnings:

### Information:

2	NPL Documents	020244_European_OA.pdf	272338	no	3
			7e39dc92014c8decc485154c24f9cb906711f1b3		
<b>Warnings:</b>					
<b>Information:</b>					
<b>Total Files Size (in bytes):</b>				1036768	
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